



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **SATOH, Yoshihiro**

Serial No.: **09/981,402**

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Title: **Semiconductor Device and Method
for Its Manufacture**

Attorney Docket No.: **N320402-00W**

Group Art Unit: **2815**

Examiner: **Richards, N. D.**

**AMENDMENT AFTER FINAL ACTION
(37 C.F.R. §1.116)**

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

A. Introductory Comments

The following is submitted in response to the FINAL Office Action dated June 2, 2004, and is currently due October 4, 2004 with a 1-month extension.

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37 C.F.R. §1.8

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Date of Deposit: September 27, 2004

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